

WHAT IS CLAIMED IS:

1. A solid-state image pickup apparatus comprising:

a solid-state image pickup device for generating an  
5 information charge in response to an image of which light was  
received;

a drive circuit for transferring said information charges  
accumulated in said solid-state image pickup device, and  
outputting said information charge;

10 a power supply for generating a predetermined voltage in  
accordance with the input amount of voltage booster pulses and  
supplying the voltage to said solid-state image pickup device  
and said drive circuit; and

a pulse generator circuit for generating and supplying  
15 said voltage booster pulses to said power supply;

wherein said pulse generator circuit stops generation of  
said voltage booster pulses in accordance with the termination  
of the information charge readout operation of one screen  
pickup period from said image pickup device, and when said  
20 drive circuit executes the electronic shutter operation, which  
discharges the information charge that has accumulated in said  
solid-state image pickup device to resume the accumulation,  
said voltage booster pulses are generated over a predetermined  
voltage booster period prior to said electronic shutter  
25 operation at a higher frequency than said readout period to  
boost the voltage of said power supply.

2. The solid-state image pickup apparatus according to claim 1 wherein:

a shutter timing is set for performing said electronic shutter operation in subsequent said screen pickup period in accordance with an exposure condition in arbitrary said image pickup period; and

a start timing is set for starting to performing said voltage boosting operation prior to said shutter timing by a predetermined time at least as long as said voltage booster period.

3. The solid-state image pickup apparatus according to claim 1 wherein:

a shutter trigger pulse having a predetermined pulse width at least as long as said voltage booster period is used;

said voltage boosting operation is initiated in connection with the timing of the leading edge of said shutter trigger pulse; and

said electronic shutter operation is initiated in connection with the timing of the trailing edge of said shutter trigger pulse.

4. The solid-state image pickup apparatus according to claim 1 wherein:

said stop operation of said voltage booster pulse circuit is prohibited when the start timing of said voltage boosting

operation precedes the termination of said readout period of said information charge.